



**ELECTRONIC
INNOVATIONS**
IN ACTION

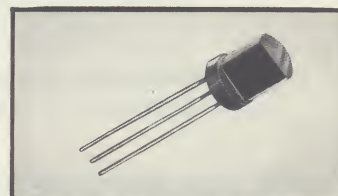
SEMICONDUCTORS

Silicon Monolithic Darlington Amplifiers

PLANAR EPITAXIAL PASSIVATED

35.55 3/67

D16P1
D16P2
D16P3
D16P4



Features

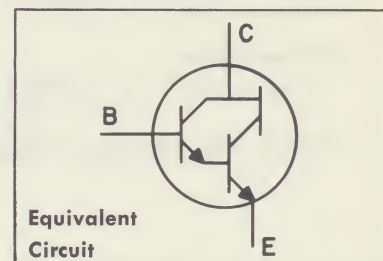
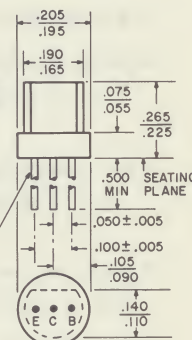
- Low Cost • Very High Beta
- High Input Impedance

DIMENSIONS WITHIN
JEDEC OUTLINE TO-98

NOTE 1: Lead diameter is controlled in the zone between .070 and .250 from the seating plane. Between .250 and end of lead a max. of .021 is held.

ALL DIMEN. IN INCHES AND ARE
REFERENCE UNLESS TOLERANCED

3 LEADS
.017 +.002
-.001
(NOTE 1)



The General Electric D16P1, D16P2, D16P3 and D16P4 are planar epitaxial passivated NPN silicon Darlington monolithic amplifiers. They are ideal for preamplifier input stages requiring high input impedances of several megohms.

absolute maximum ratings: (25°C) (unless otherwise specified)

		D16P1 D16P2	D16P3 D16P4	
Voltages				
Collector to base	V_{CBO}	18	40	V
Collector to emitter	V_{CEO}	12	20	V
Collector to emitter	V_{CES}	18	40	V
Emitter to base	V_{EBO}	8	12	V
Current				
Collector (steady state)	I_C	200	200	mA
Base (steady state)	I_B	20	20	mA
Dissipation				
Total power (free air @ 25°C)	P_T	320	320	mW
Total power (free air @ 65°C)*	P_T	185	185	mW
Temperature				
Storage	T_s	-65 to +150		°C
Operating	T_j	-65 to +120		°C
Lead $\frac{1}{16}$ " \pm $\frac{1}{32}$ " from case for 10 seconds maximum	T_L	260	260	°C

*Derate 3.4 mW/°C for increase in ambient temperature between 25 and 120°C.

electrical characteristics: (25°C) (unless otherwise specified)

STATIC CHARACTERISTICS

			Min.	Typ.	Max.
Collector cutoff current					
($V_{CE} = 18V, V_{BE} = 0$)	D16P1, 2	I_{CES}			100 nA
($V_{CE} = 18V, V_{BE} = 0, T_j = 100^\circ C$)	D16P1, 2	I_{CES}			20 μA
($V_{CE} = 40V, V_{BE} = 0$)	D16P3, 4	I_{CES}			100 nA
($V_{CE} = 40V, V_{BE} = 0, T_j = 100^\circ C$)	D16P3, 4	I_{CES}			20 μA
Emitter cutoff current					
($V_{EB} = 8V$)	D16P1, 2	I_{EBO}			100 nA
($V_{EB} = 12V$)	D16P3, 4	I_{EBO}			100 nA

GENERAL ELECTRIC

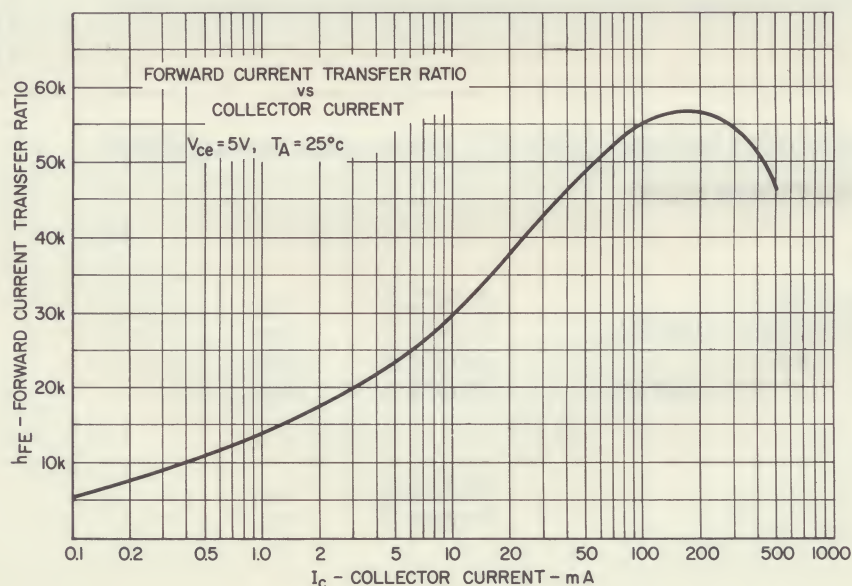
electrical characteristics (25°C): (unless otherwise specified) (cont'd)

			Min.	Typ.	Max.
Collector emitter breakdown voltage					
(I _C = 10 mA, I _B = 0)	D16P1, 2	BV _{CEO}			12 V
	D16P3, 4	BV _{CEO}			20 V
Forward current transfer ratio					
(I _C = 2 mA, V _{CE} = 5V)	D16P1, 3	h _{FE}	2,000		
	D16P2, 4	h _{FE}	7,000	70,000	
(I _C = 100 mA, V _{CE} = 5V)	D16P1, 3	h _{FE} †	10,000		
	D16P2, 4	h _{FE} †	20,000		
Collector emitter saturation voltage					
(I _C = 200 mA, I _B = 0.2 mA)	D16P1, 2	V _{CE(sat)} †			1.4 V
	D16P3, 4	V _{CE(sat)} †			1.0 V
Base emitter saturation voltage					
(I _C = 200 mA, I _B = 0.2 mA)		V _{BE(sat)} †			1.5 V
Base emitter drive voltage					
(I _C = 200 mA, I _B = 0.2 mA)		V _{BE} †			1.5 V

DYNAMIC CHARACTERISTICS

Forward current transfer ratio					
(I _C = 2 mA, V _{CE} = 5V, f = 1 kHz)	D16P1, 3	h _{fe}	2,000		
	D16P2, 4	h _{fe}	7,000	15,000	
Input impedance					
(I _C = 2 mA, V _{CE} = 5V, f = 1 kHz)	D16P1, 2	h _{ie}			500 kΩ
	D16P3, 4	h _{ie}			650 kΩ
Forward current transfer ratio					
(I _C = 2 mA, V _{CE} = 5V, f = 20 MHz)		h _{fe}	3	8.4	
Output capacitance					
(V _{CB} = 10V, f = 1 MHz)		C _{ebo}		7.6	10 pF
Input capacitance					
(V _{EB} = 0.5V, f = 1 MHz)		C _{ebo}		10.5	pF

†Pulsed Measurement: Pulse width ≤ 300 μ sec., Duty cycle ≤ 2%



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INNOVATIONS
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SEMICONDUCTORS

Semiconductor Application Information

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Supersedes 200.01 2/67

NEW
APPLICATION NOTES!
SEE REVERSE SIDE

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General References:

- 200.0 Abstracts of Application Notes, Articles and Manuals
- 450.52 Published product Specification Sheets
- 640.13 Product catalog with major specifications

1. General Applications for Signal Semiconductors

- 90.13 Some Large Signal Properties of Planar and Epitaxial Transistors
- 90.15 An Integrated Reference Amplifier for Precision Power Supplies
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- 90.20 A Precision Current Supply using the Reference Amplifier
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- 90.60 Application of Multi-pellet Diodes in DTL Circuits
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- 90.62 Y Parameters: Their Accuracy and Measurement
- 90.65 Optoelectronics—A new Dimension in Circuit Design

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- 90.58 Reversible Ring Counter Utilizing the Silicon Controlled Switch
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- 200.42 Commutation Behaviour of Diffused High Current Rectifier Diodes
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- 660.10 SCR's Break the Frequency Barrier
- 660.12 Better Utilization of SCR Capability with A-C Inductive Loads
- 660.13 The Rating and Application of SCR's Designed for Power Switching at High Frequencies
- 660.14 Basic Magnetic Functions in Converters and Inverters Including New Soft Commutation
- 671.1 Economy Power Semiconductor Applications
- CP65-937 A Program Report on Solid State Appliance Controls
- ETR-3960 G-E Electronic Components Hobby Manual (\$1.50)*
- ETR-3875A Silicon Controlled Rectifier Manual, 4th Ed., \$3.00*

3. Transistor Circuits

- 90.3 Efficient High Quality Program Amplifier Circuits using the Industrial Silicon Series 2N2107, 2N2108 and 2N2196
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- 450.37 Transistor Manual—7th Edition (Order by ETR 3296, \$2.00)*

4. Silicon Controlled Rectifier Circuits

- 200.14 The Silicon Controlled Rectifier in Lamp Dimming and Heating Control Service

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6. Tunnel Diode Applications

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A supplement to *Semiconductor Applications*, publication number 200.0

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By O.A. Kolody 32 Pages

Theory of operation and the physics of light emitting devices and detectors is discussed. Device characteristics and circuit applications of optoelectronic devices such as injection lasers, visible and infrared light emitting diodes, photo diodes, phototransistors, light activated SCR's and SCS's are considered. Pertinent terminologies, design equations and procedures are included.

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By D.R. Grafham 40 Pages

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200.33 12/66

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By D.R. Grafham 24 Pages

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200.49 2/67

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By N. Mapham 20 Pages

A single SCR inverter circuit is described which minimizes the cost of semiconductors and associated trigger circuitry. It is most useful in the power range of 250 watts to several kilowatts, and over the frequency range of 400 to 30,000 Hertz. This application note is directed to the power supply designer and uses BASIC computer language to prepare exact solutions to the circuit analysis leading to simple tabular design procedure.

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By B.W. Jalbert 16 Pages

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